**CECS 341 - Lab X**

**“”**

**Due date: 03/21/19**

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I certify that this submission is my original work

Chaz Del Prato

Lab Report: Lab Assignment X - “”

1. **Goal:** The goal of this lab is to understand how the memory stage works in a single cycle MIPS processor. Also there is logic to see if a branch will be taken.
2. **Steps:**
3. **Results:** describe the results obtained
4. **Conclusion:** what did you learn, and what were the challenges